

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~striketrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please AMEND claim 1 in accordance with the following:

1. (Currently Amended) A plasma display device, comprising:
 - a plurality of first electrodes;
 - a plurality of second electrodes disposed nearly in parallel with said plurality of first electrodes so as to configure ~~a display cell-cells~~, each display cell including one of the plurality of first electrodes and one of the plurality of second electrodes together therewith, and so as to activate electric discharge between ~~themselves~~ the one of the plurality of second electrodes and a the one of the plurality of first electrode-electrodes comprising said display cell;
 - a first electrode drive circuit applying discharge voltage to said plurality of first electrodes;
 - and
 - a second electrode drive circuit applying discharge voltage to said plurality of second electrodes, wherein
 - said first and second electrode drive circuits comprise first and second sustain circuits, respectively, outputting a sustain discharge voltage for activating electric discharge associated with light emission in said display cell, and at least one of said first or second sustain circuits has a parallel circuit in which a first switching element having a high-speed switching performance and a second switching element having a low-saturation-voltage performance are connected in parallel, thereby applying the sustain discharge voltage to both the first and second switching elements.
2. (Original) The plasma display device according to claim 1, wherein said first switching element is a power MOSFET.
3. (Original) The plasma display device according to claim 1, wherein said second switching element is an IGBT.
4. (Original) The plasma display device according to claim 1, wherein said first switching

element is a power MOSFET, and said second switching element is an IGBT.

5. (Original) The plasma display device according to claim 1, wherein said second switching element is turned on at least during a period that discharge current flows between said first electrodes and said second electrodes.

6. (Original) The plasma display device according to claim 5, wherein said first switching element is a power MOSFET.

7. (Original) The plasma display device according to claim 5, wherein said second switching element is an IGBT.

8. (Original) The plasma display device according to claim 5, wherein said first switching element is a power MOSFET, and said second switching element is an IGBT.

9. (Currently Amended) The plasma display device according to claim 1, wherein said first switching element and said second switching element are comprised so that a drive voltage is applied to an electrode at different timings.

10. (Original) The plasma display device according to claim 9, wherein said first switching element is a power MOSFET.

11. (Original) The plasma display device according to claim 9, wherein said second switching element is an IGBT.

12. (Original) The plasma display device according to claim 9, wherein said first switching element is a power MOSFET, and said second switching element is an IGBT.

13. (Previously Presented) The plasma display device according to claim 9, wherein at least one of said first or second sustain circuits comprises a higher-potential-side switching circuit supplying a first potential in relation to said sustain discharge voltage to said electrodes configuring said display cell, and a lower-potential-side switching circuit supplying a second potential in relation to said sustain discharge voltage, lower than said first potential;
said higher-potential-side switching circuit and said lower-potential-side switching

circuit respectively having said parallel circuit in which said first switching element and said second switching element are connected in parallel.

14. (Original) The plasma display device according to claim 13, wherein said first switching element is a power MOSFET.

15. (Original) The plasma display device according to claim 13, wherein said second switching element is an IGBT.

16. (Original) The plasma display device according to claim 13, wherein said first switching element is a power MOSFET, and said second switching element is an IGBT.

17. (Previously Presented) The plasma display device according to claim 13, wherein said first and second electrode drive circuits further comprise a power recovery circuit connected to said first electrode configuring said display cell.

18. (Previously Presented) The plasma display device according to claim 13, wherein said first and second electrode drive circuits further comprise a power recovery switch connected via a coil to said first electrode configuring said display cell.

19. (Original) The plasma display device according to claim 18, wherein said second switching element is turned on at least during a period that discharge current flows between said first electrodes and said second electrodes.

20. (Original) The plasma display device according to claim 18, wherein said first switching element is a power MOSFET.

21. (Original) The plasma display device according to claim 18, wherein said second switching element is an IGBT.

22. (Original) The plasma display device according to claim 18, wherein said first switching element is a power MOSFET, and said second switching element is an IGBT.

23. (Original) The plasma display device according to claim 1, wherein said first

switching element and said second switching element almost coincide with each other in their input threshold voltage characteristics.

24. (Original) The plasma display device according to claim 1, wherein said first switching element and said second switching element are driven based on the same drive signal.

25. (Original) The plasma display device according to claim 1, wherein a switching time of said first switching element is shorter than that of said second switching element.

26. (Previously Presented) The plasma display device according to claim 13, wherein said higher-potential-side switching circuit is configured so as to supply a positive potential in relation to said sustain discharge voltage to the electrode configuring said display cell, and said lower-potential-side switching circuit is configured so as to supply a negative potential in relation to said sustain discharge voltage to the first electrode configuring said display cell.

27. (Original) The plasma display device according to claim 26, wherein said positive potential represents a voltage which equals to a half of said sustain discharge voltage above the ground level, and said negative potential represents a voltage which equals to a half of said sustain discharge voltage below the ground level.

28. (Previously Presented) The plasma display device according to claim 26, wherein said first and second electrode drive circuits further comprise a power recovery circuit connected to said first electrode configuring said display cell.

29. (Previously Presented) The plasma display device according to claim 26, wherein said first and second electrode drive circuits further comprise a power recovery switch connected via a coil to said first electrode configuring said display cell.

30. (Original) The plasma display device according to claim 29, wherein said positive potential represents a voltage which equals to a half of said sustain discharge voltage above the ground level, and said negative potential represents a voltage which equals to a half of said sustain discharge voltage below the ground level.

31. (Original) The plasma display device according to claim 30, wherein one terminal of said power recovery switch is connected via said coil to said electrode configuring said display cell, and the other terminal is connected to a ground terminal.

32. (Previously Presented) The plasma display device according to claim 13, wherein a reset voltage for initializing said display cell is superposed to the reference voltage of said lower-potential-side switching circuit during a period that said reset voltage is supplied to said first electrode configuring said display cell.

33. (Previously Presented) The plasma display device according to claim 32, wherein said first and second electrode drive circuits further comprise a power recovery circuit connected via a coil to said first electrode configuring said display cell.

34. (Previously Presented) The plasma display device according to claim 33, wherein one terminal of said power recovery switch is connected via a coil to said electrode configuring said display cell, and

a voltage synchronized with said reset voltage for initializing said display cell is superposed to the other terminal of said power recovery switch during a period that said reset voltage is supplied to said first electrode configuring said display cell.